

FACULTY PROFILE

Name : **Dr.K.Nagarathna Rajur**



Designation : **Associate Professor**
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Vidwan Id : 170012

Researcher ID (Web of Science) : AAH-9897-2021

Google Scholar ID : o_HoLIIAAAAJ

Qualification : **Ph.D**

Professional

Experience : 23years

Teaching experience : 2 years

Industry experience

Administrative : 1.Project Co-ordinator

Responsibilities

Teaching :

No. of Projects

Guided

UG : 10

PG : 1

Research

Interest Area : 1.Machine Learning(Block Chain Technology)
2.WireLess Sensor Networks.

- Patents** : 3. Machine Learning (Data Analysis)
: 1. patent numbered 202141016943
Blockchain Enabled Hardware system for IoT infrastructure Integraon for Security opmizaon .
- Publications** : 1. Essentials of Security in Internet of Things, By KINDDLE Publications
Books/Chapters ASIN : B094423D7F (Complete book)
2. Energy Efficient Time Synchronization in WSN, By LAMBERT (LAP) academic publications. ISBN: 978-620-2-79388-9. (Complete Book)
3. Advances in Intelligent Systems and Computing, Vol 347 , Springer, (one chapter.).
4. Emerging Research in Computing, Information , Communication and Application, Vol 1 Springer (one chapter.).
- Journals citations) (with :**
1. K. Nagarathna :” IoT combined with Block-Chain and 5G infrastructure for its application and development : A Review” International Journal Of Engineering And Computer Science (ijecs) Volume 10 Issue May 2021, Page No. 25327-25335 ISSN: 2319-7242 DOI: 10.18535/ijecs/v10i5.4582. www.ijecs.in
 2. K. Nagarathna : “ Energy-aware strategy for data forwarding in IoT ecosystem” International Journal of Electrical and Computer Engineering (IJECE) Vol. 10, No. 5, October 2020, pp. 4863~4871 ISSN: 2088-8708, DOI: 10.11591/ijece.v10i5.pp4863-4871 .
 3. K. Nagarathna and J. D. Mallapur, : “ An Investigational Analysis of different Approaches and Techniques for Time Synchronization in Wireless Sensor Network” International Journal of Computer Applications (0975 – 8887) Volume 103 – No.5, October 2014, page No 18 – 28,
 4. K. Nagarathna and J. D. Mallapur “TFTS: “A Novel Triple Factor Time Synchronization for Effective Routing in Large Scale WSN” An International Journal of Wireless Personal Communication (2016) (ISSN 0929-6212) Volume 87 Number 4, page No : 1157–1173 , DOI 10.1007/s11277-015-3046-2
 5. K. Nagarathna and J. D. Mallapur : “OTSA : Optimized Time Synchronization Approach for Delay -based Energy Efficient Routing in WSN ” An (IJACSA) International Journal of Advanced Computer Science and Applications, Vol. 8, No. 4, 2017 , Digital Object Identifier (DOI) : 10.14569/IJACSA.2017.080446 . , page no: 331-341
 6. K. Nagarathna and Renuka Bariker : “Design and Development of Adaptive Routing Algorithm to Reduce Distortion During Video Transmission” International Journal of Electronics and Communication Engineering and Technology. (IJECET) (ISSN Print: 0976-6464 and ISSN Online: 0976-6472) Volume 7, Issue 4, July-August 2016, pp. 01–12, Article ID: ECET_07_04_001
- Conferences** : 1. K. Nagarathna and J. D. Mallapur : “LNLCS: Leveraging Network

Lifetime using Clock Synchronization in Wireless Sensor Network “
International Conference on Electrical, Electronics, and Optimization
Techniques (ICEEOT) – 2016, 978-1-4673-9939-5/16/\$31.00 ©2016 IEEE.

2. K. Nagarathna and J.D.Mallapur : “An Optimized & On-Demand Time Synchronization in Large Scale Wireless Sensor Network: OOD-TS “
© Springer India 2015 N.R.Shetty et.al.(eds) . Emerging Research in Computing Information and Communication and Applications ,DOI 10.1007/987-81-322-2550-8-29
3. K.Nagarathna and J.D.Mallapur: “E2TS: Energy Efficient Time Synchronization Technique in Large Scale Wireless Sensor Network “
© Springer International Publishing Switzerland 2015 R. Silhavy et al. (eds.), Artificial Intelligence Perspectives and Applications, 225 Advances in Intelligent Systems and Computing 347, DOI: 10.1007/978-3-319-18476-0_23
4. J. D. Mallapur, M. I. Sataraddi, S. C. Hiremath and K. Nagarathna:“Priority Based Scheduler for Bluetooth Network”,
Proceedings of International Conference on Advances in Communication, Computing and Control, (ICAC3), 18th - 19th January 2013, Fr. Conceicao Rodrigues College of Engineering, Bandra, Mumbai, CCIS 361, pp. 356-365.
5. J D. Mallapur, Kiran Y. B, K. Nagarathna, S. Hiremath: “Trust Based Secured Routing in Wireless Multimedia Sensor Networks” Fourth International Conference on Computational Intelligence, Communication Systems and Networks, 24 - 26 July Pucket , pp 53-58, 2012.

Events Organized

- : 1. SDP in Campus on PCB design, 18 -19 May, 2015 funded by TEQIP – II.

**Conferences/
Symposiums/
Workshops/ Training
Programs Attended**

- : 1 Artificial Intelligence National Institute of Technology Kurukshetra. Five days 2020-9-26 to 2020-9-30
- 2 Artificial Intelligence Vellore Institute of Technnology, Vellore Five days 2020-9-21 to 2020-9-25
- 3 NEXT GENERATION ELECTRONICS AND COMPUTER NETWORKS BKIT,Bhalki Five days 2nd To 6th June 2020
- 4 Digital Transformation In Teaching Learning Process NPIU TEQIP-3 IIT Bombay Two Weeks Feb 14th To March 6th 2020
- 5 Machine Learning & Artificial Intelligence ELECTRONICS & ICT ACADEMY National Institute Of Technology WARANGAL(T.S), INDIA. Sphoorthy Engineering College One week 2nd Dec – 7th Dec 2019

6	NPTEL workshop	IIT Madras	One day	29 th Nov 2019
7	Honour Code CERTIFICATE	IIT Bombay	Eight months	01 May 2019 TO 31 Dec 2019
8	FDP on "IOT & Embedded System Design"	In Association with Ministry of Electronics & Information Technology (MeitY), Government of India Electronics & ICT Academy IIT Guwahati, and TI University Programme of DIGITAL SHARK TECHNOLOGY.	Five days	24- 28th September, 2018
9	Faculty Development Programme	IIT Madras	Three days	12-14 Sep 2016
10	Telecom Management: Current and Emerging	IIT Delhi	Two days	20-21 Aug 2015
11	Training on rapid simulation and performance evaluation of current and future networks construct's	Nihon Communication solutions, Bang lore	Two days	19-20 Feb 2012
12	Advances in DBMS and Future Allied Research Avenues	KLE Institute of Technology, Hubli	Three Days	24-26 Feb 2011
13	Training Of Trainers on Enterpreneurial Motivation	SDMCET , Dharwad	One Day	8 th Oct 2010
14	Multicore Programming	Intel Centre , Bangalore	Three Days	27-29 Jan 2010
15	Web Programming	BIET, Davanagere	Three days	25-27 June 2009
16	Research Opportunities in the area of Digital Image Processing.	SDMCET , Dharwad.	Three Days	13-15 March 2009
17	Work shop on embedded systems using Microcontroller.	BVB Hubli	Five Days	18-22 Dec 2008

18	Introduction to Quantum Computers and Algorithms.	SDMCET , Dharwad	One day	17 th May 2008
19	Networking Technology (WONET)	BVCET, Hubli	Three Days	29-31 Oct 2004